

The course on Analog and Digital Electronics aims to provide students with a fundamental understanding of the principles of electronic circuits and their applications in both analog and digital systems.

LEARNING OUTCOMES:

On successful completion of this course, the student will be able to:

1. Understand Principles and Working of Operational Amplifier
2. Apply their knowledge on OP-Amp in different Applications
3. To understand the number systems, Binary codes and Complements.
4. To understand the Boolean algebra and simplification of Boolean expressions.
5. To analyze logic processes and implement logical operations using combinational logic circuits.
6. To understand the concepts of sequential circuits and to analyze sequential systems in terms of state machines

UNIT-I: OPERATIONAL AMPLIFIERS

a) Concept of feedback in CE amplifier, negative and positive feedback, advantages and disadvantages of negative feedback, Basic concepts of differential amplifier, Block diagram of op amp and its equivalent circuit, IC Diagram (IC 741), Ideal voltage transfer curve, Open loop Op-Amp configurations- differential, inverting and non-inverting Op-Amps.

b) Voltage Series Feedback Amplifier (Non-Inverting Op amp): Gain and Bandwidth derivations: Voltage Shunt Feedback Amplifier (Inverting Op amp): Gain and Bandwidth derivations

UNIT-II: PRACTICAL OPERATIONAL AMPLIFIER AND APPLICATIONS

a) Characteristics of an Ideal and Practical Operational Amplifier (IC 741), Input offset voltage, Input bias current, Input offset current, total output offset voltage, CMRR, slew rate and concept of virtual ground.

b) Applications of Op-Amp: Linear Applications: Voltage Follower, Summing Amplifier, Subtracting Amplifier, Averaging Amplifier, Difference Amplifier, Integrator and Differentiator, Square Wave response of Integrator and Differentiator (Brief explanation only)

UNIT-III: NUMBER SYSTEMS, CODES AND LOGIC GATES

a) Number Systems and Codes: Decimal, Binary, Octal and Hexadecimal number systems, conversions, Binary addition, Binary subtraction using 1's and 2's complement methods, BCD code and Gray code – Conversions

b) Logic Gates: Construction and truth tables of OR, AND, NOT gates, Universal gates – Basic construction and truth tables of NOR & NAND, Realization of logic gates using NAND and NOR, XOR and XNOR Logic gates symbol and their truth tables. De Morgan's Laws, Boolean Laws, Simplification of Boolean Expressions using Boolean Laws

UNIT-IV: ARITHMETIC CIRCUITS & DATA PROCESSING CIRCUITS

- a) Half Adder and Full Adder: Explanation of truth tables and Circuits. Half Subtractor and Full Subtractor: Explanation of truth tables and Circuits, 4 - bit binary Adder/Subtractor.
- b) Multiplexers - 2 to 1 Multiplexer, 4 to 1 multiplexer, De-multiplexers: 1 to 2 Demultiplexer, 1 to 4 Demultiplexer, Applications of Multiplexers and Demultiplexers Decoders: 1 of 2 decoders, 2 of 4 decoders, Encoders: 4 to 2 Encoder, 8 to 3 Encoder, Applications of decoders and encoders

UNIT-V: SEQUENTIAL LOGIC CIRCUITS & CODE CONVERTERS

- a) Combinational Logic vs Sequential Logic Circuits, Sequential Logic circuits: Flip-flops, Basic NAND, NOR Latches, Clocked SR Flip-flop, JK Flip-flop, D Flip-flop, Master-Slave Flip-flop, Conversion of Flip flops.
- b) Code Converters: BCD to Decimal Converter, BCD to Gray Code Converter, BCD to 7 segment Decoders

Reference Books:

1. OP-Amps and Linear Integrated Circuit, R. A. Gayakwad, 4th edition, 2000, Prentice Hall
2. Operational Amplifiers and Linear ICs, David A. Bell, 3rd Edition, 2011,
3. Digital Principles and Applications, A.P. Malvino, D.P. Leach and Saha, 7th Ed., TMH
4. Fundamentals of Digital Circuits, Anand Kumar, 2nd Edn, 2009, PHI Learning Pvt. Ltd.
5. Thomas L. Floyd, Digital Fundamentals, Pearson Education Asia (1994)
6. R. L. Tokheim, Digital Principles, Schaum's Outline Series, Tata McGraw- Hill (1994)

SEMESTER-III
COURSE 8: ANALOG AND DIGITAL ELECTRONICS

Practical

Credits: 1

2 hrs/week

COURSE OBJECTIVES:

The course objectives for a practical course in Analog and Digital Electronics might provide students with hands-on experience in designing, constructing, and testing analog and digital electronic circuits.

LEARNING OUCOMES:

1. Understand the principles of analog and digital electronic circuits and their applications in real-world scenarios.
2. Analyze and design analog electronic circuits using diodes, transistors, and operational amplifiers.
3. Analyze and design digital electronic circuits using logic gates, flip-flops, and counters.
4. Understand the importance of biasing, feedback, and stability in electronic circuits and how to achieve them.
5. Develop the skills to design and analyze amplifier circuits and digital systems.

Minimum six experiments to be done and recorded.

1. To study the operational amplifier as inverting feedback amplifier with verifying gain
2. To study the operational amplifier as non-inverting feedback amplifier with verifying gain
3. To study operational amplifier as adder
4. To study operational amplifier as subtractor
5. To study operational amplifier as differentiator
6. To study operational amplifier as integrator
7. Logic Gates- OR, AND, NOT and NAND gates. Verification of Truth Tables.
8. Verification of De Morgan's Theorems.
9. Construction of Half adder and Full adders-Verification of truth tables
10. Flip flops
11. Multiplexer and De-multiplexer
12. Encoder and Decoder

STUDENT ACTIVITIES

UNIT-I: OPERATIONAL AMPLIFIERS

Circuit Analysis: Students can be asked to analyze different operational amplifier circuits such as inverting and non-inverting amplifiers, summing amplifiers, difference amplifiers, and integrators. They can be asked to calculate the gain, input and output impedance, and frequency response of the circuits.

Circuit Design: Students can be asked to design different operational amplifier circuits such as audio amplifiers, filters, and oscillators. They can be asked to select the appropriate op-amp and other components such as resistors, capacitors, and inductors to meet the desired specifications.

UNIT-II: PRACTICAL OPERATIONAL AMPLIFIER AND APPLICATIONS

Design an inverting amplifier circuit: Students can be asked to design and build an inverting amplifier circuit using an operational amplifier and a few passive components. They can then measure the gain and frequency response of the circuit using an oscilloscope and a function generator. They can also compare the measured values with the theoretical calculations and simulation results.

Build a summing amplifier circuit: Students can be asked to build a summing amplifier circuit using an operational amplifier and several input signals. They can then measure the output voltage of the circuit and compare it with the expected value. They can also investigate the effect of changing the input signal amplitudes and the resistor values on the circuit performance.

UNIT-III: NUMBER SYSTEMS, CODES AND LOGIC GATES

Convert numbers between different bases: Students can be asked to convert numbers between binary, decimal, and hexadecimal bases. They can practice converting both integer and fractional numbers, and verify their results using online conversion tools or calculators.

Design a binary adder circuit: Students can be asked to design and build a binary adder circuit using logic gates such as XOR, AND, and OR gates. They can then test the circuit by adding two binary numbers and comparing the result with the expected value.

UNIT-IV: ARITHMETIC CIRCUITS & DATA PROCESSING CIRCUITS

Design a data processing circuit: Students can be asked to design and build a data processing circuit that performs a specific function, such as filtering, modulation, or demodulation. They can use op-amps, filters, modulators, and demodulators to implement the circuit and test its performance using simulated or real-world signals.

Implement a digital signal processing algorithm: Students can be asked to implement a digital signal processing algorithm, such as a Fourier transform, a discrete cosine transform, or a digital filter. They can use software tools such as MATLAB or Python to simulate the algorithm and test its performance using sample signals.

UNIT-V: SEQUENTIAL LOGIC CIRCUITS & CODE CONVERTERS

Design a flip-flop circuit: Students can be asked to design and build a flip-flop circuit using logic gates and test its operation by creating a sequence of logic signals. They can also compare the performance of different types of flip-flops, such as SR, D, JK, and T, and discuss their advantages and disadvantages in sequential circuits.

Implement a counter circuit: Students can be asked to design and build a counter circuit that counts up or down using flip-flops. They can use different types of counters, such as ripple, synchronous, or Johnson, and test their operation by connecting the output to LEDs or other indicators.

Design a code converter circuit: Students can be asked to design and build a code converter circuit that converts a binary code to another code, such as Gray code, BCD, or ASCII. They can use logic gates, multiplexers, and decoders to implement the circuit, and test its operation by inputting different codes

SEMESTER-IV
COURSE 9: ELECTRICITY AND MAGNETISM

Theory

Credits: 3

3 hrs/week

COURSE OBJECTIVE:

The course on Electricity and Magnetism aims to provide students with a fundamental understanding of the principles of electricity, magnetism, and their interactions

LEARNING OUTCOMES:

On successful completion of this course, the students will be able to: